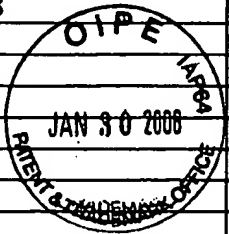
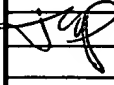


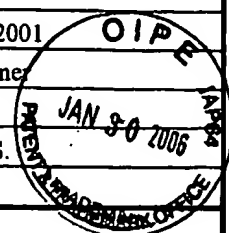
<b>Substitute for Form 1449/PTO</b>  <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>		
				Application Number	09/966,015	
				Filing Date	September 27, 2001	
				First Named Inventor	Vincent J. Zimmer	
				Art Unit	2123	
				Examiner Name	Proctor, Jason S.	
				Attorney Docket Number	42390P11198	
Sheet	1	of		2		
<b>U.S. PATENT DOCUMENTS</b>						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
			Number-Kind Code <sup>2</sup> (if known)			
	1	US-	6,075,938	06-13-2000	Bugnion et al.	
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<b>FOREIGN PATENT DOCUMENTS</b>								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	2	WO	02/052404	A2	07-04-2002	Intel Corporation		

Examiner Signature: 	Date Considered: 4/3/06
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				Art Unit	2123
				Examiner Name	Proctor, Jason S.
Sheet	2	of	2	Attorney Docket Number	42390P11198



**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
	3	EBICIOGLU, K. et al., "IBM Research Report - DAISY: Dynamic Compilation for 100% Architectural Compatibility," Computer Science, 08 May 1996. <a href="http://citeseer.ist.psu.edu/2006.html">http://citeseer.ist.psu.edu/2006.html</a>	
	4	BRACKIN, S. H., "An Interface Specification Language for Automatically Analyzing Cryptographic Protocols," Proceedings., 1997 Symposium on San Diego, CA, USA, 10-11 February 1997, pp. 40-51.	
	5	DEVANBU, P. T. et al., "Techniques for Trusted Software Engineering," Proceedings of the 1998 International Conference on Kyoto, Japan, 19-25 April 1998, pp. 126-135.	
	6	EBICIOGLU, K. et al., "An Eight-Issue Tree-VLIW Processor for Dynamic Binary Translation," 16 September 1998. <a href="http://www.research.ibm.com/vliw/isca31/An%20eight-issue%20tree-VLIW%20processor%20for%20dynamic%20binary%20translation.pdf">http://www.research.ibm.com/vliw/isca31/An%20eight-issue%20tree-VLIW%20processor%20for%20dynamic%20binary%20translation.pdf</a>	
	7	ROBIN, J. S. et al., "Analysis of the Intel Pentium's Ability to Support a Secure Virtual Machine Monitor," 11 June 2000. <a href="http://citeseer.ist.psu.edu/robin00analysis.html">http://citeseer.ist.psu.edu/robin00analysis.html</a>	

Examiner Signature		Date Considered	4/3/06
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